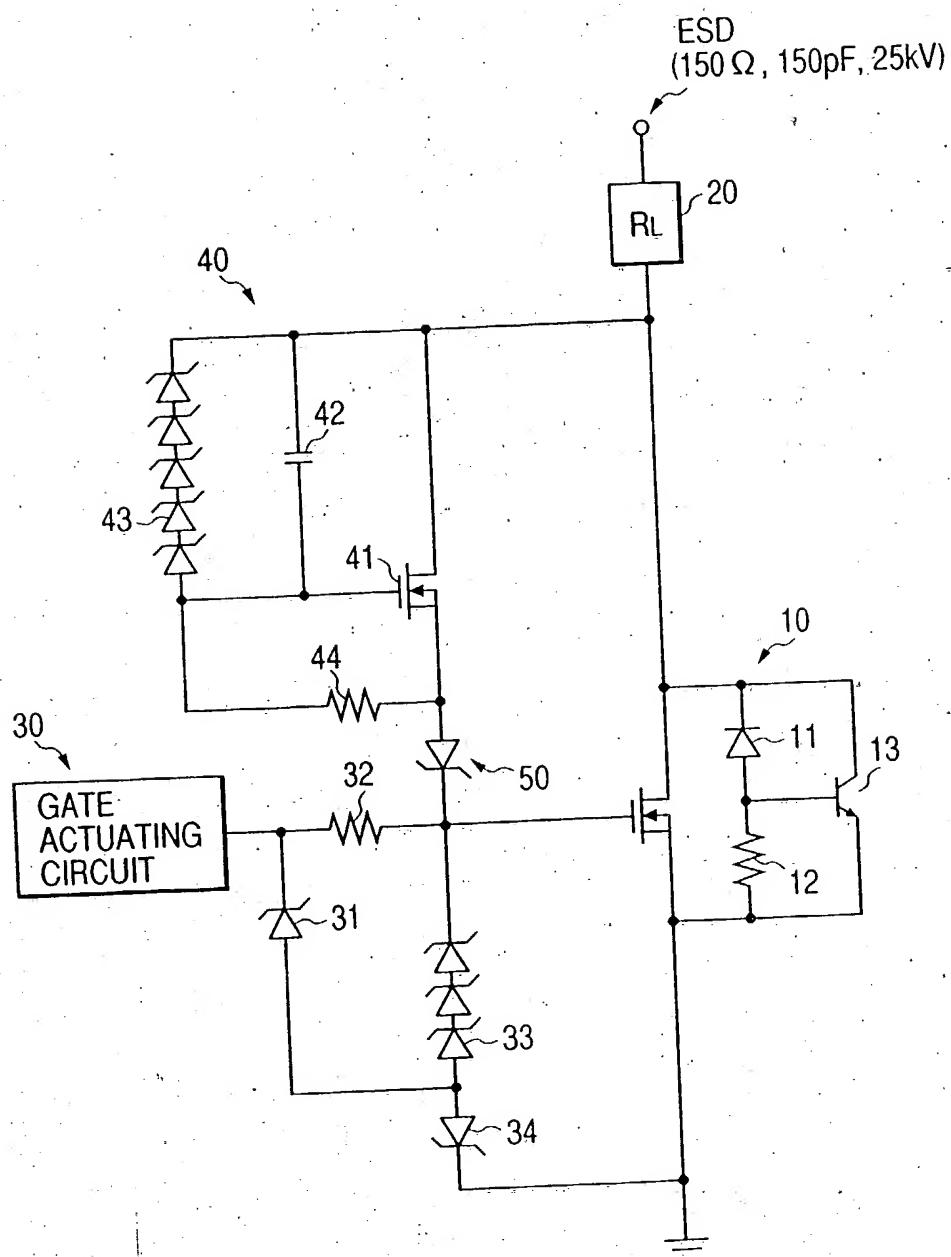


FIG. 1



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FIG. 2A

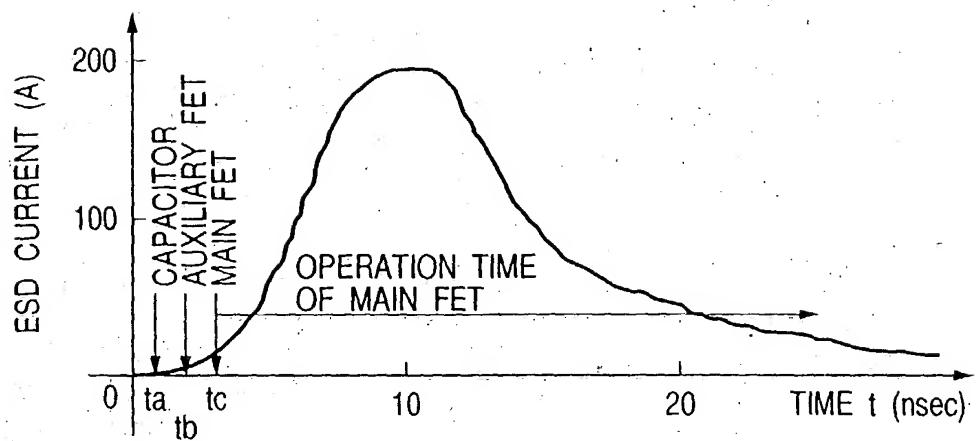


FIG. 2B

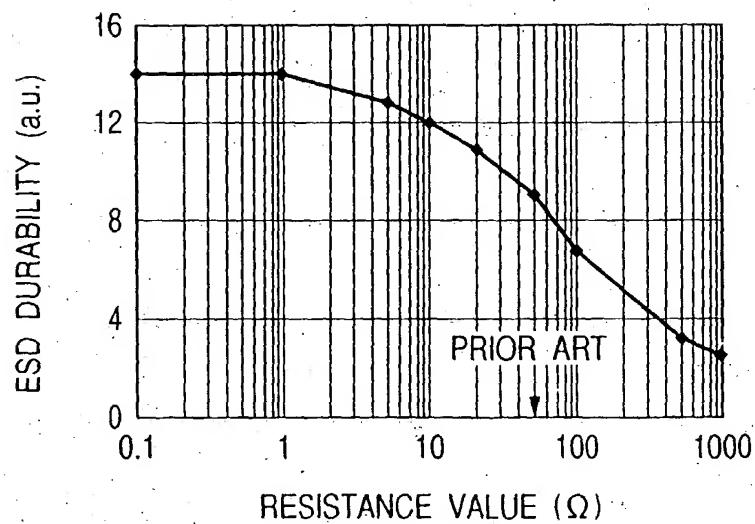
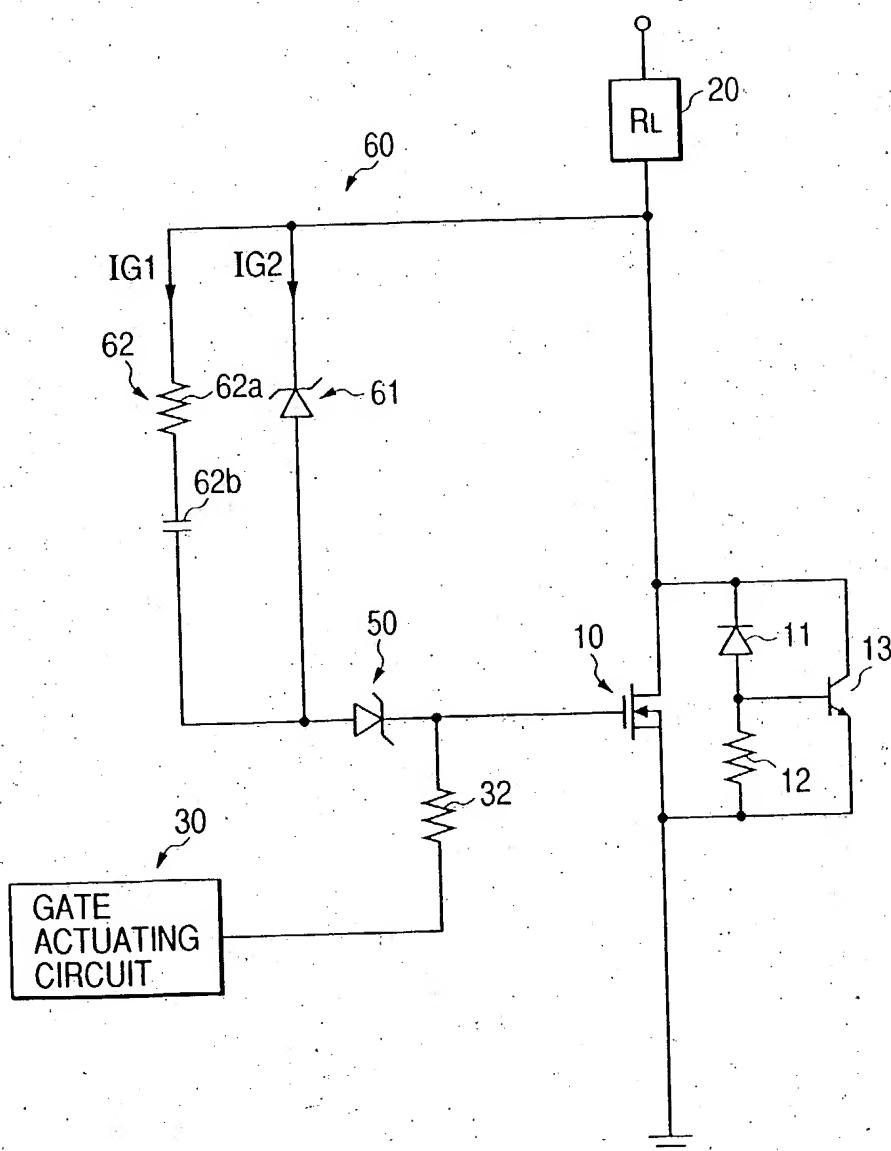


FIG. 3



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FIG. 4

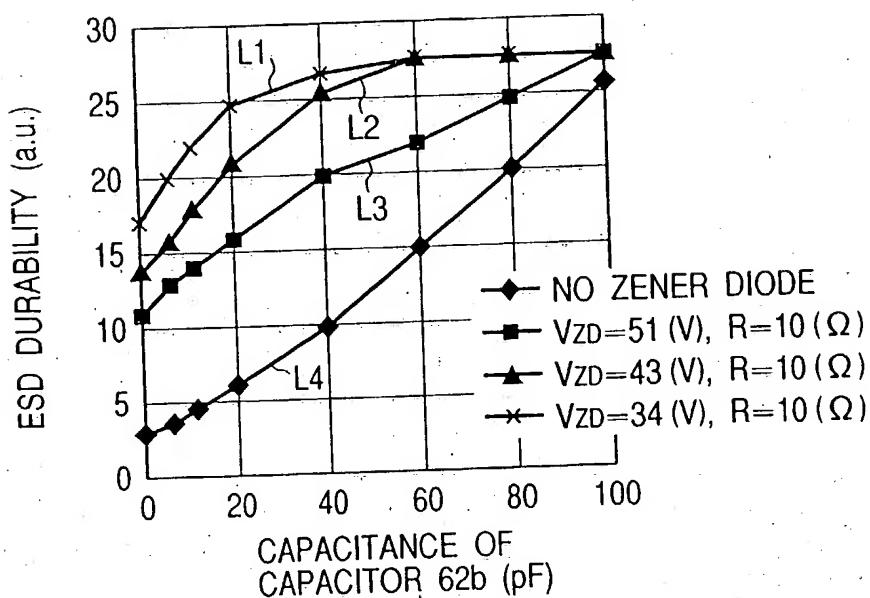
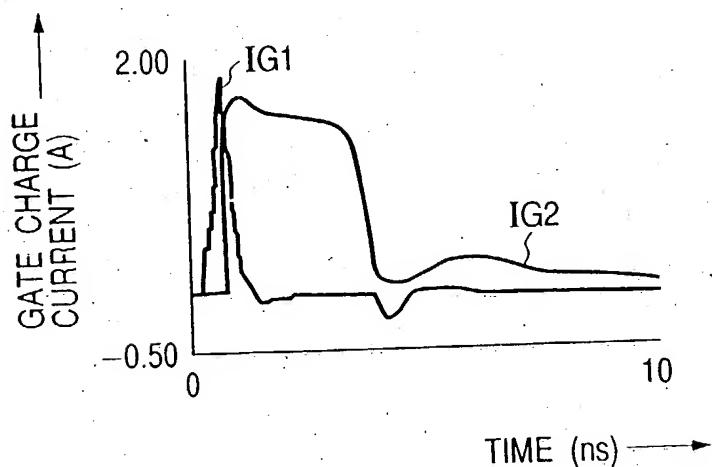


FIG. 5



Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

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FIG. 6

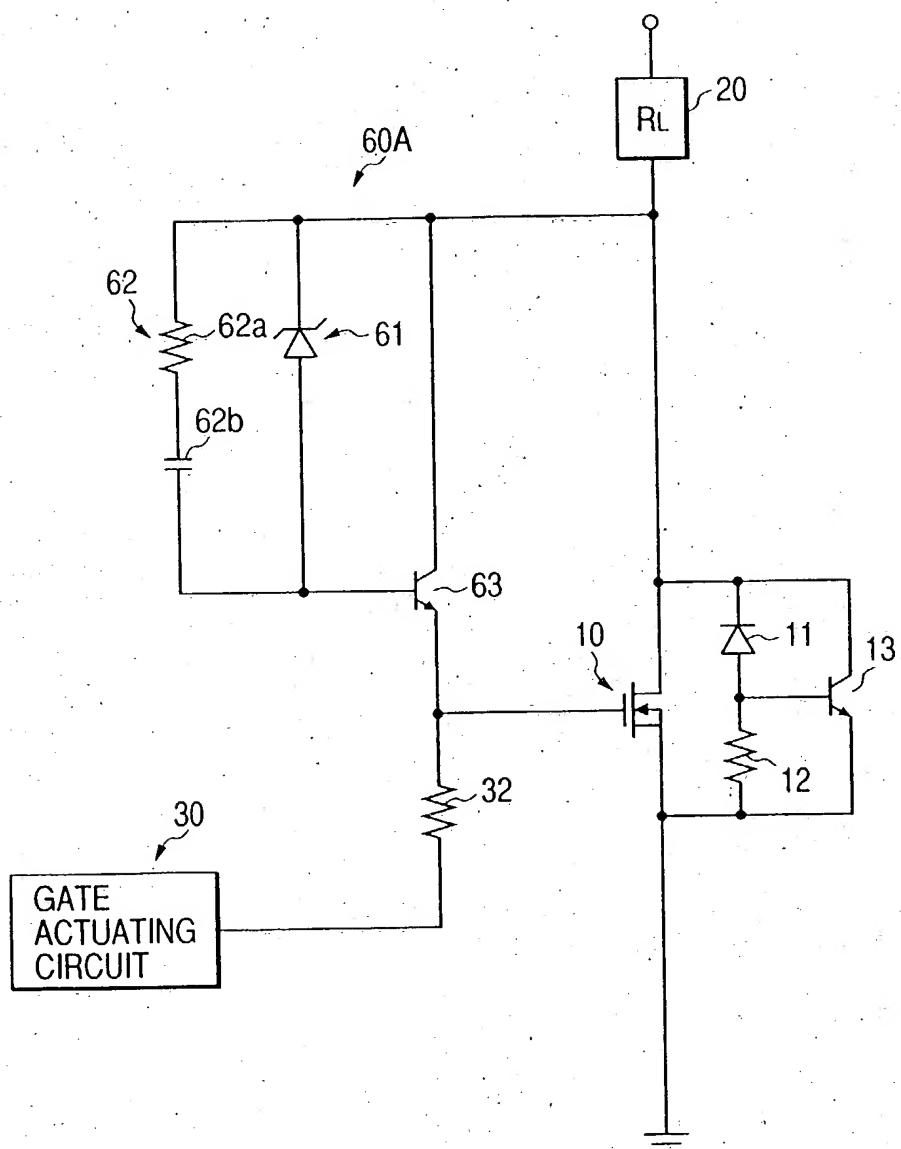
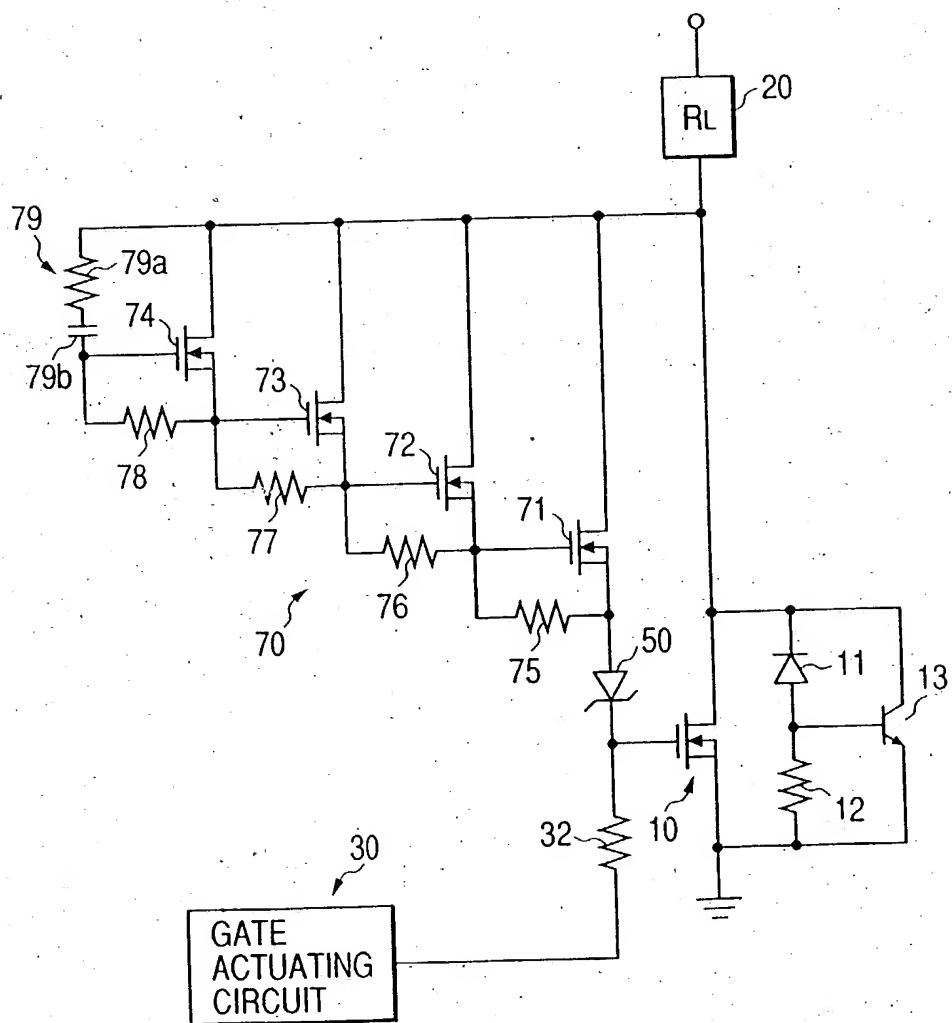


FIG. 7



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FIG. 8

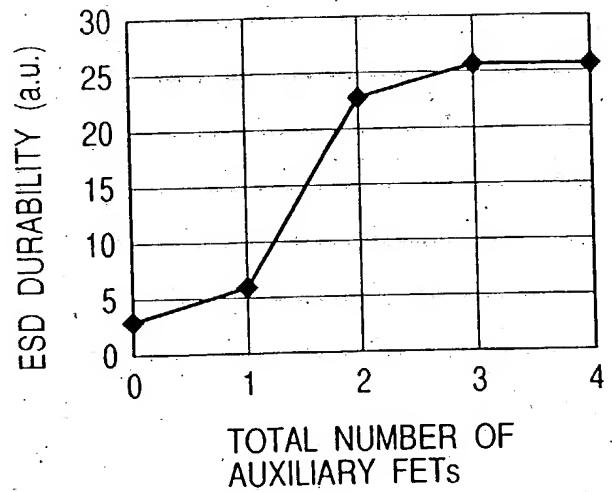


FIG. 10

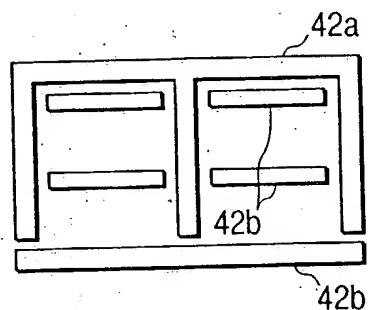


FIG. 11 PRIOR ART

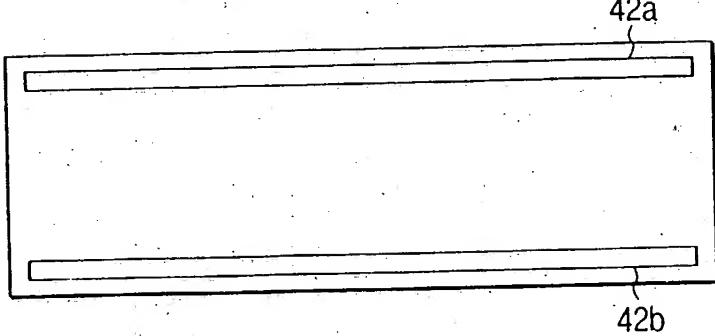
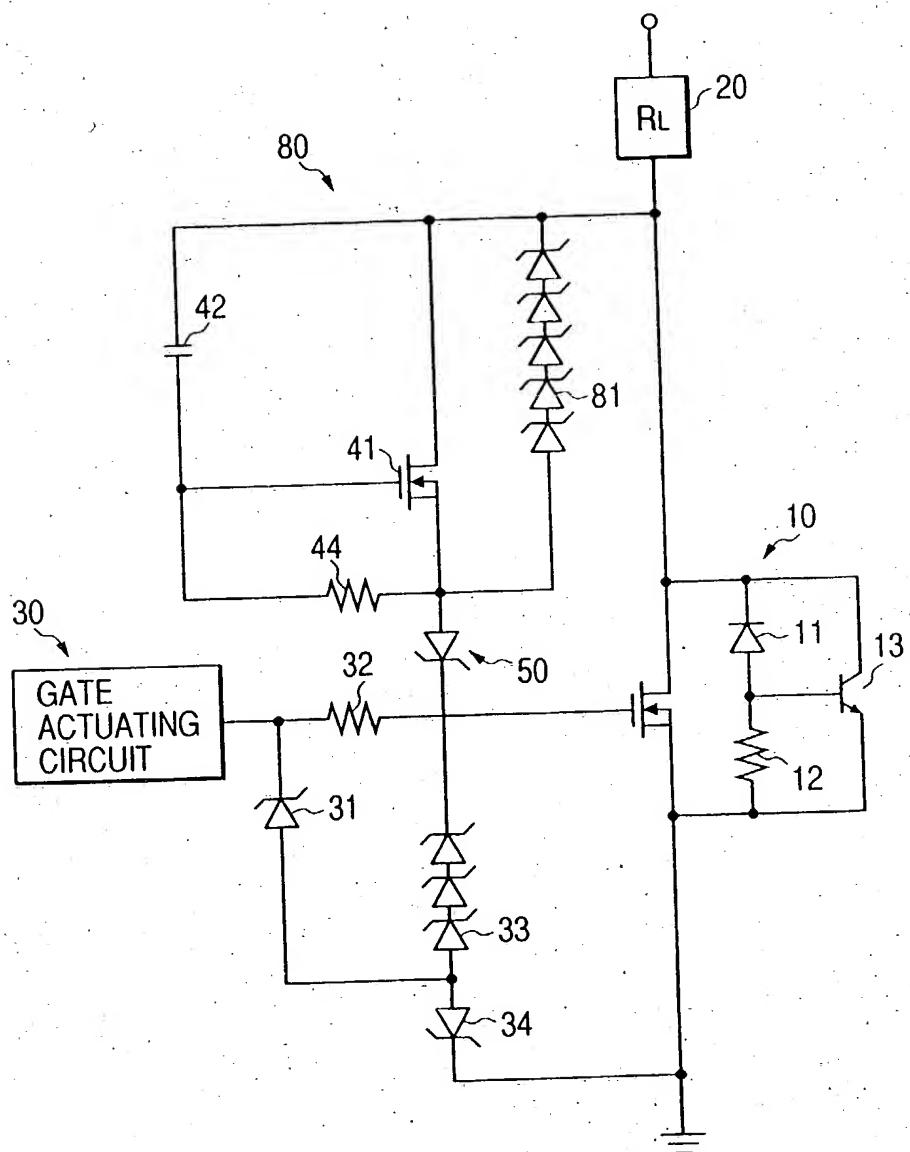


FIG. 9



Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

Atty. Ref. No.: 4041P-000020/DVA

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FIG. 12A

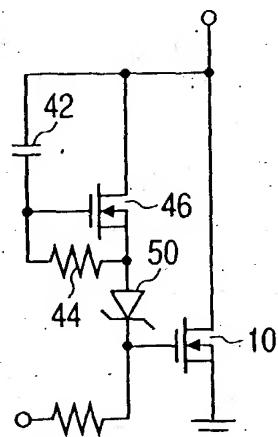


FIG. 12B

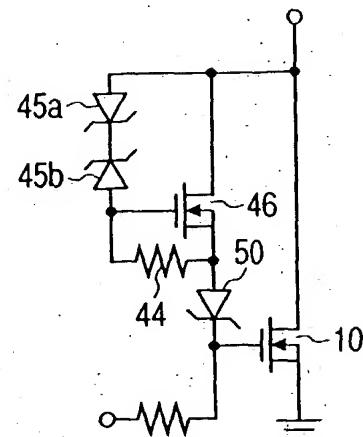
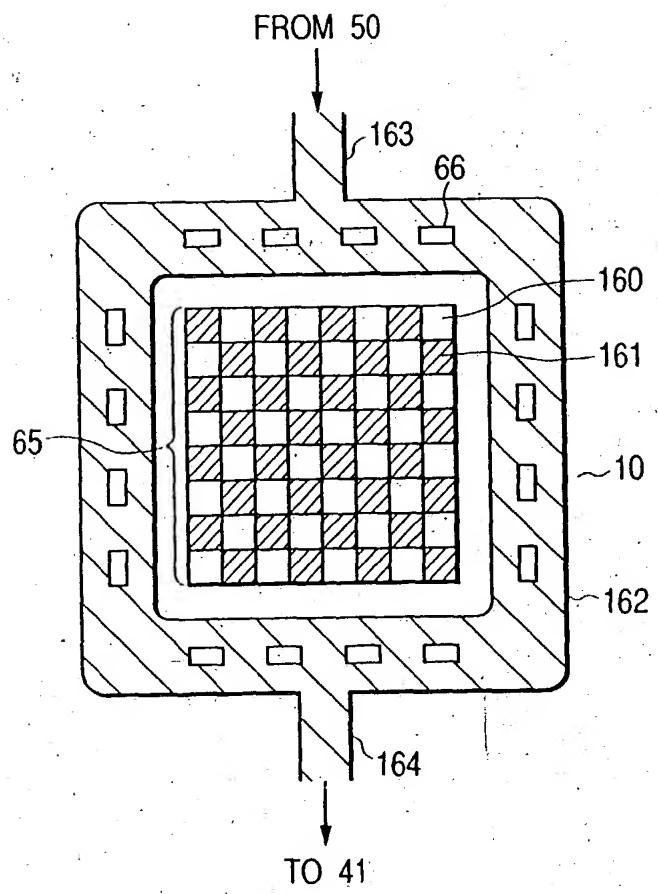


FIG. 14



Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

Atty. Ref. No.: 4041P-000020/DVA

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FIG. 13A

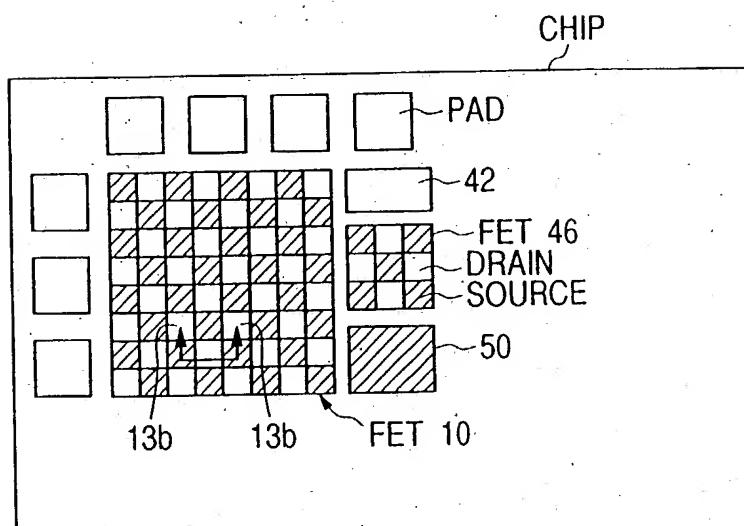
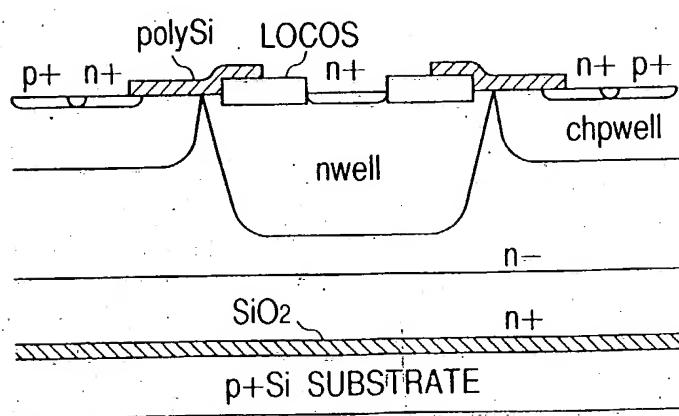


FIG. 13B



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FIG. 15A

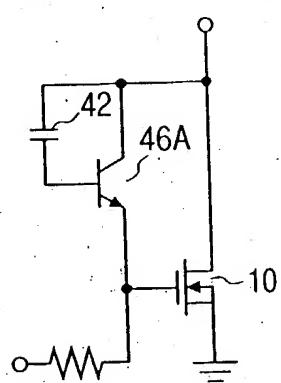


FIG. 15B

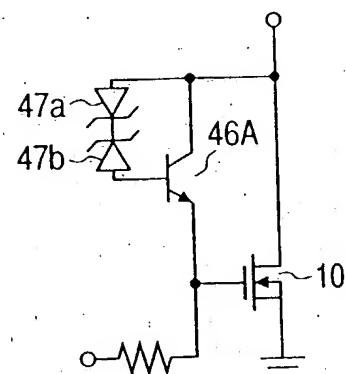


FIG. 16A

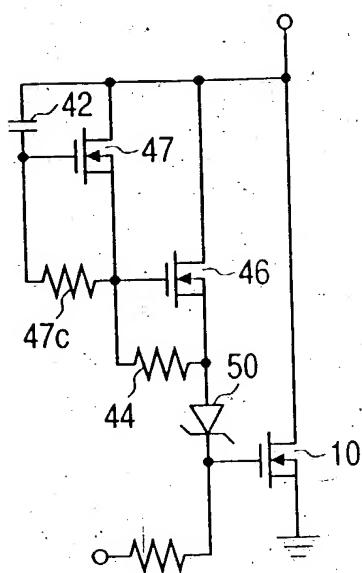
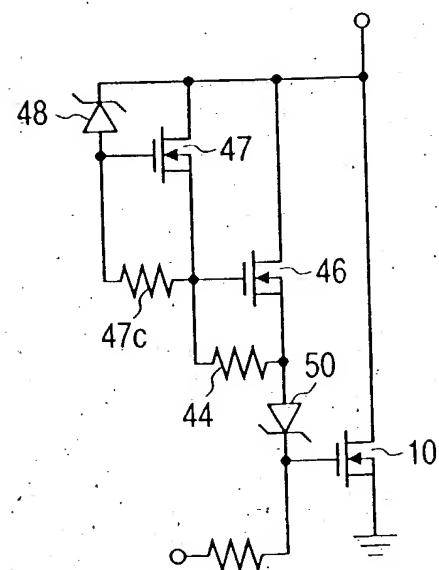


FIG. 16B



Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

Atty. Ref. No.: 4041P-000020/DVA

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FIG. 17A

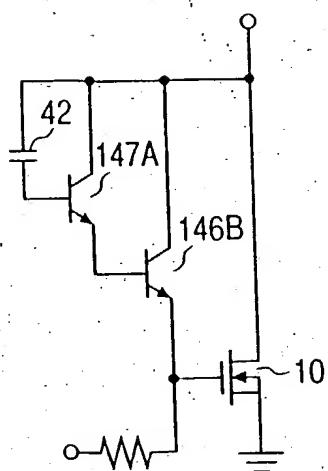


FIG. 17B

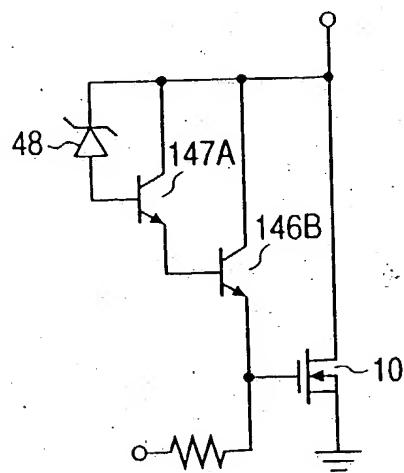


FIG. 18A

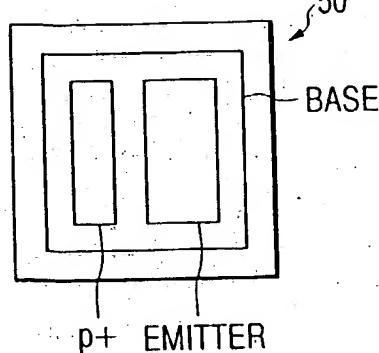


FIG. 18B
PRIOR ART

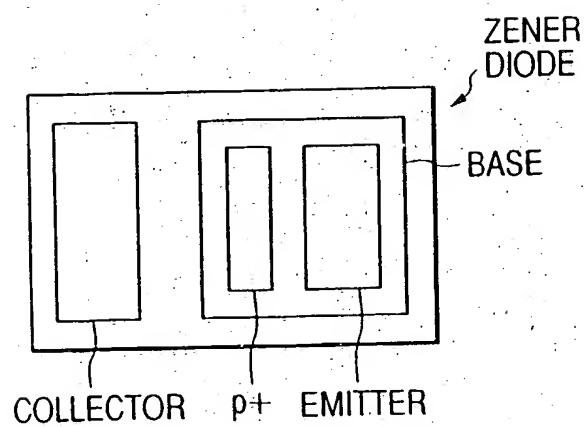
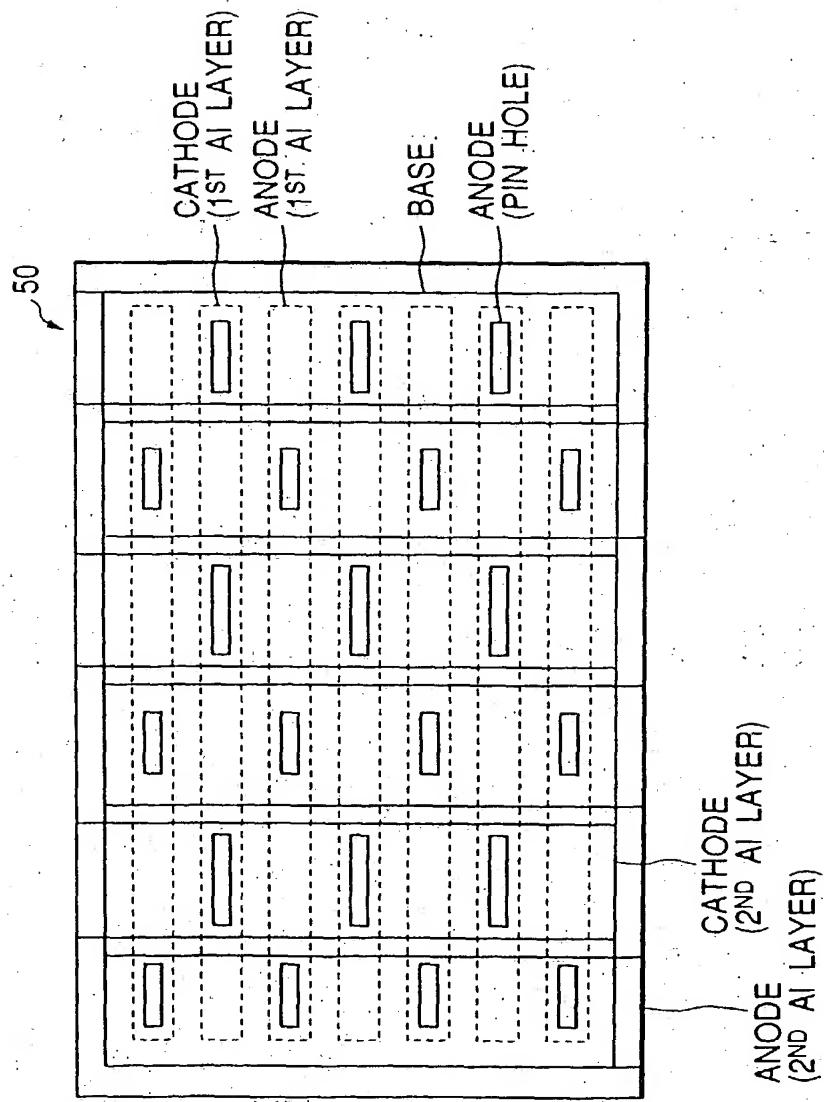


FIG. 19



Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

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FIG. 20A

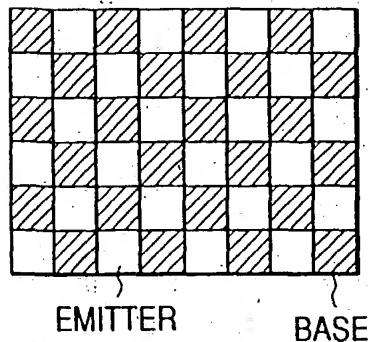


FIG. 20B

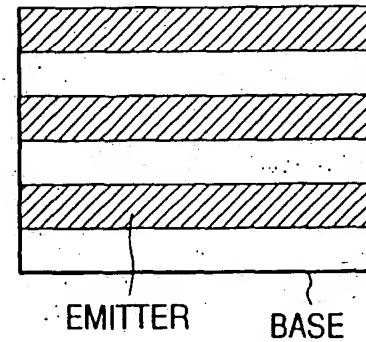


FIG. 21

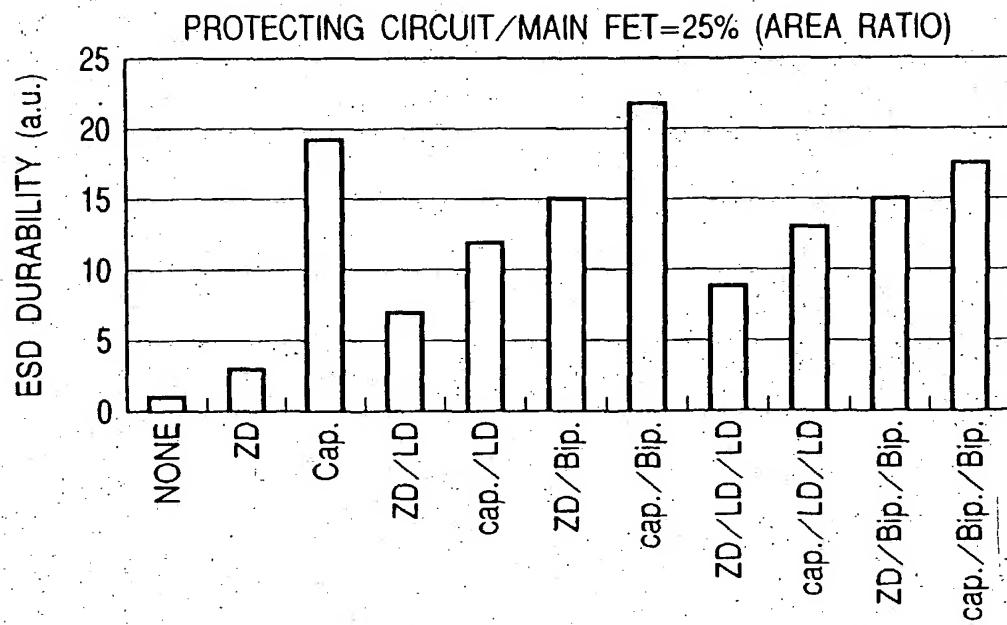
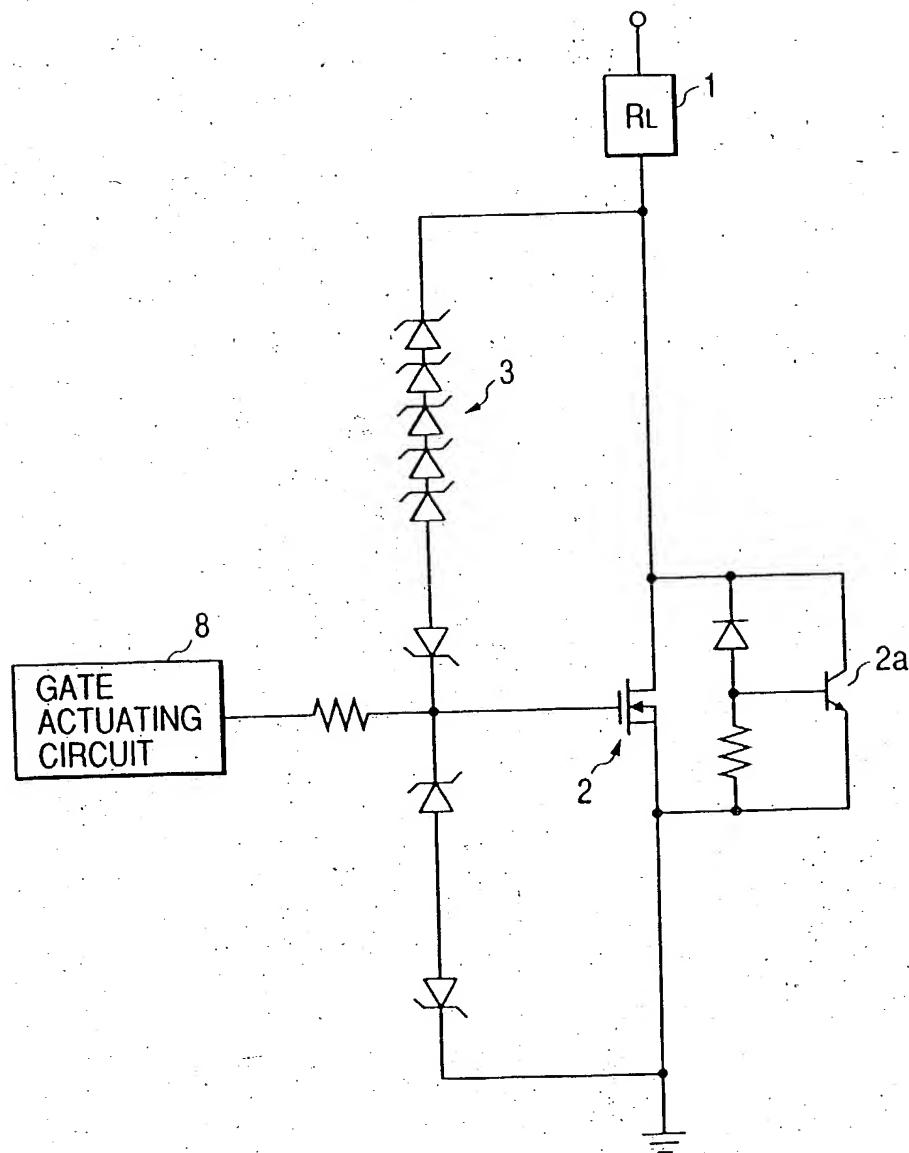


FIG. 22 PRIOR ART



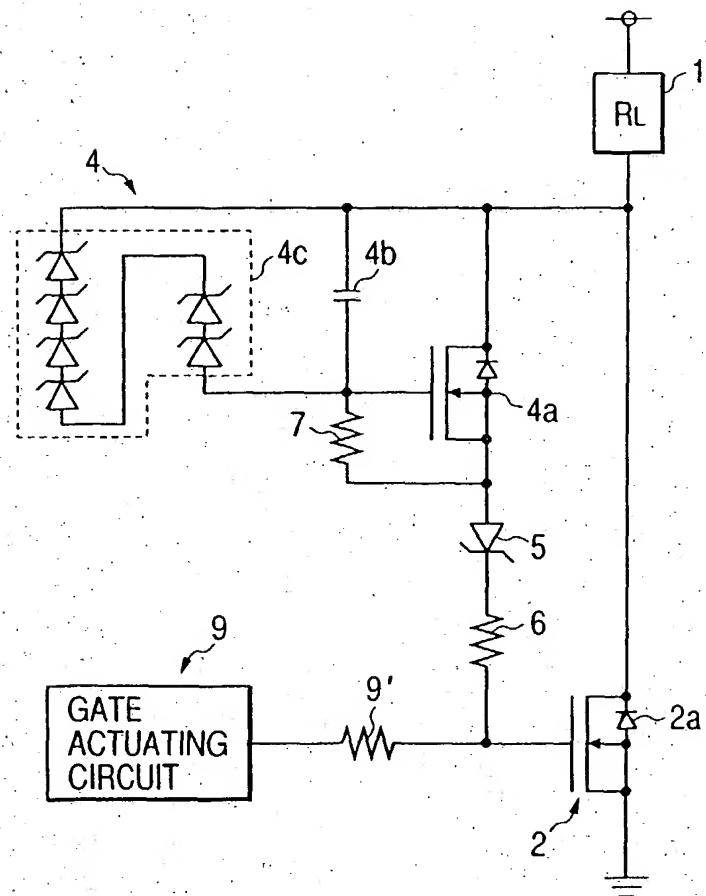
Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

Atty. Ref. No.: 4041P-000020/DVA

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FIG. 23 PRIOR ART



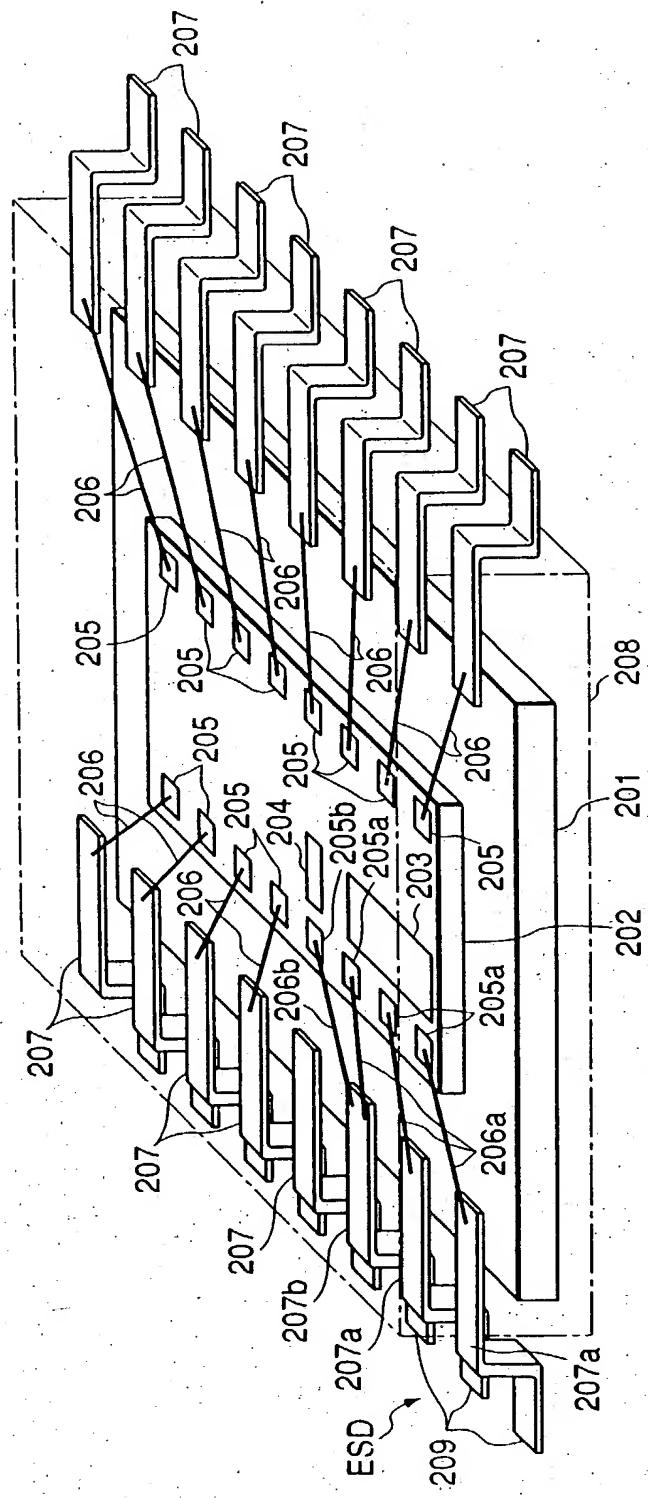
Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

Atty. Ref. No.: 4041P-000020/DVA

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FIG. 24



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FIG. 25

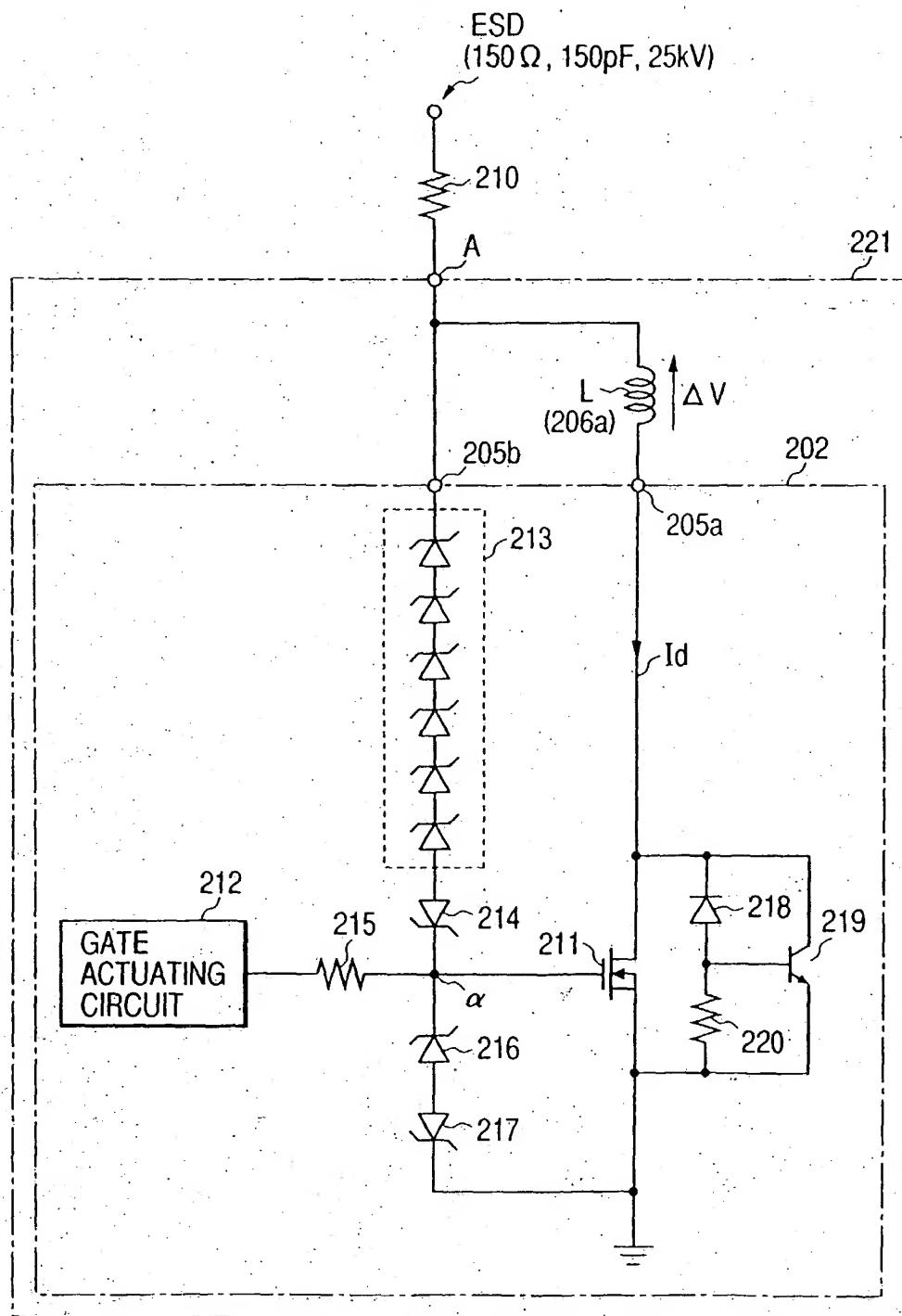
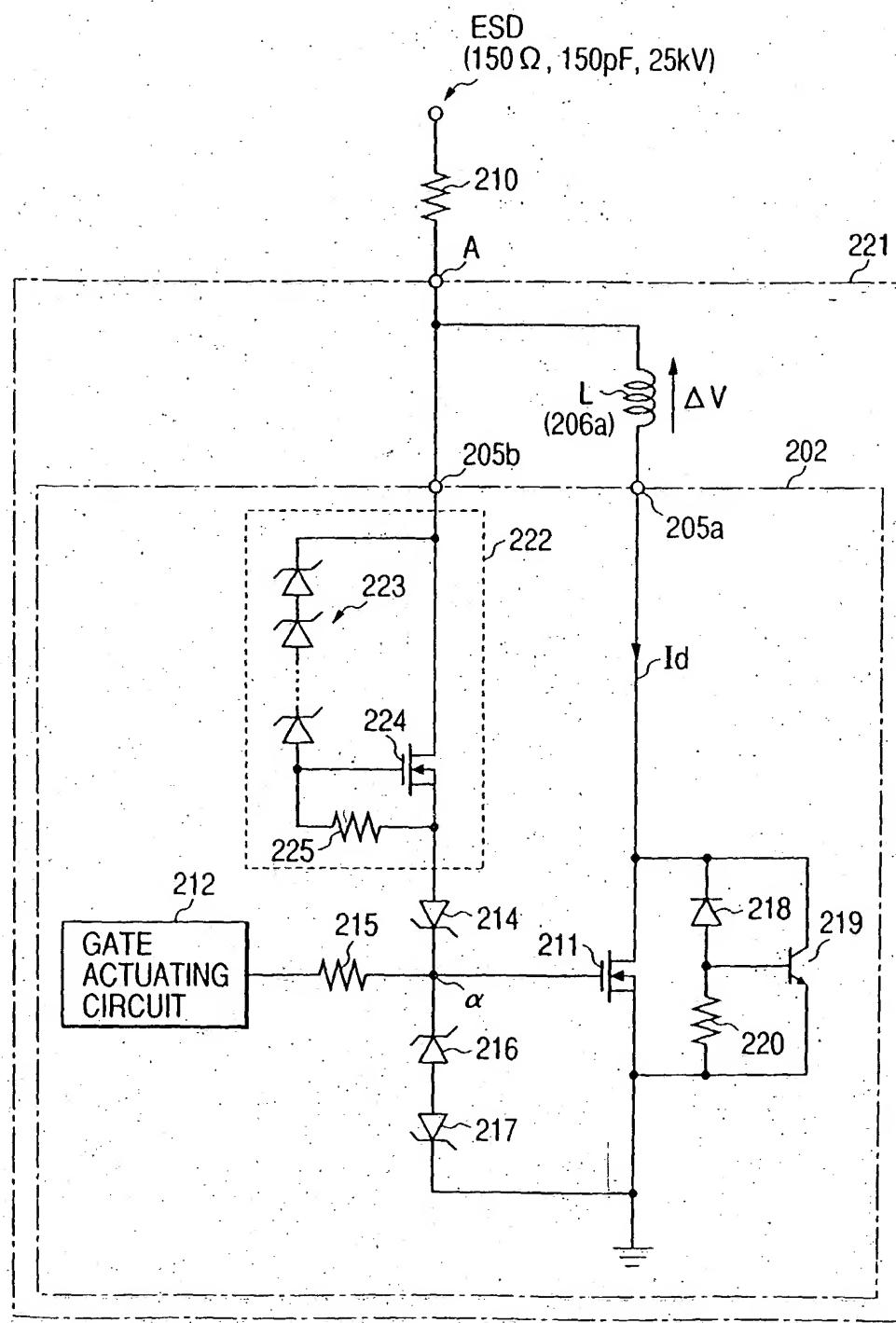


FIG. 26



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FIG. 27

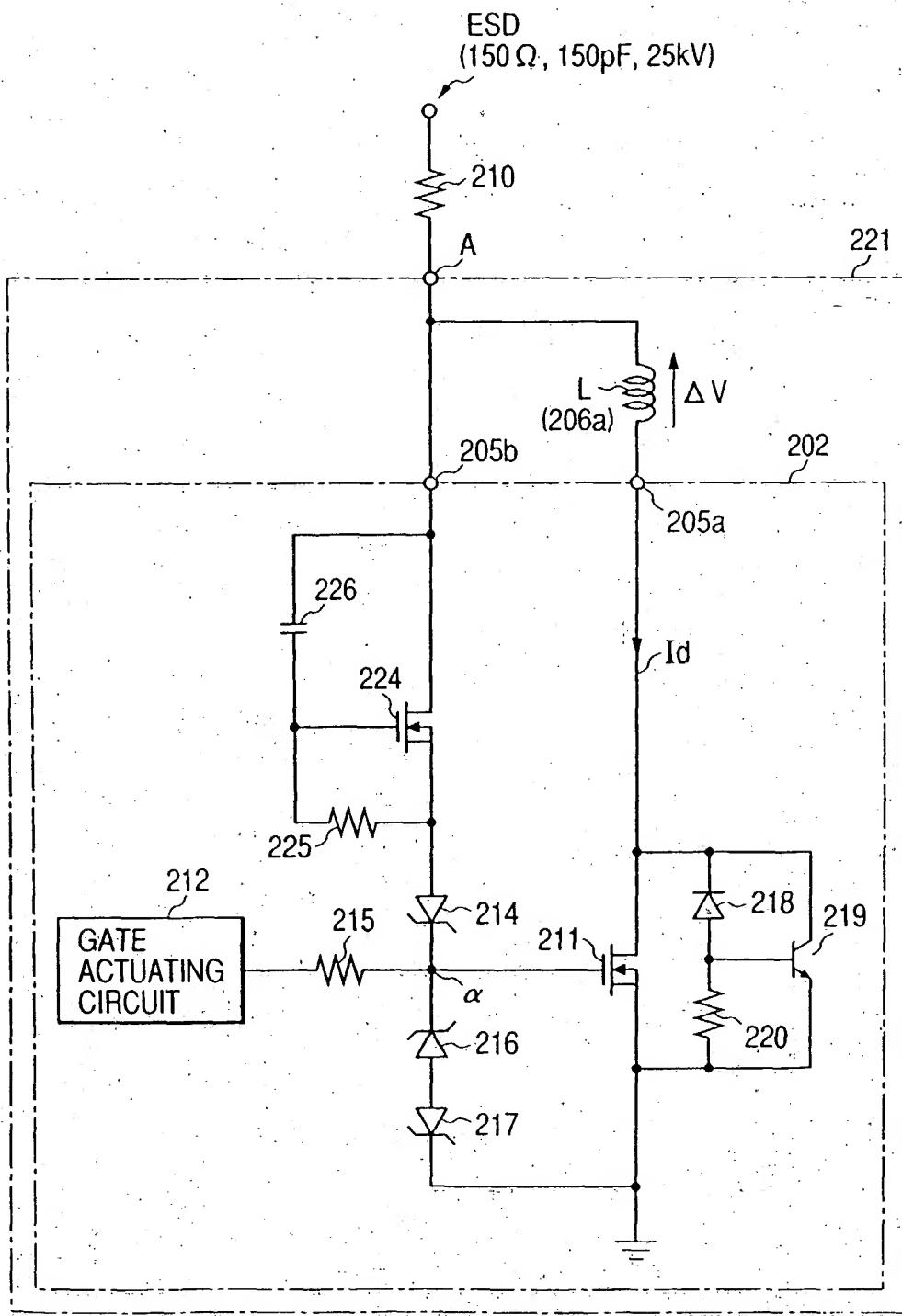
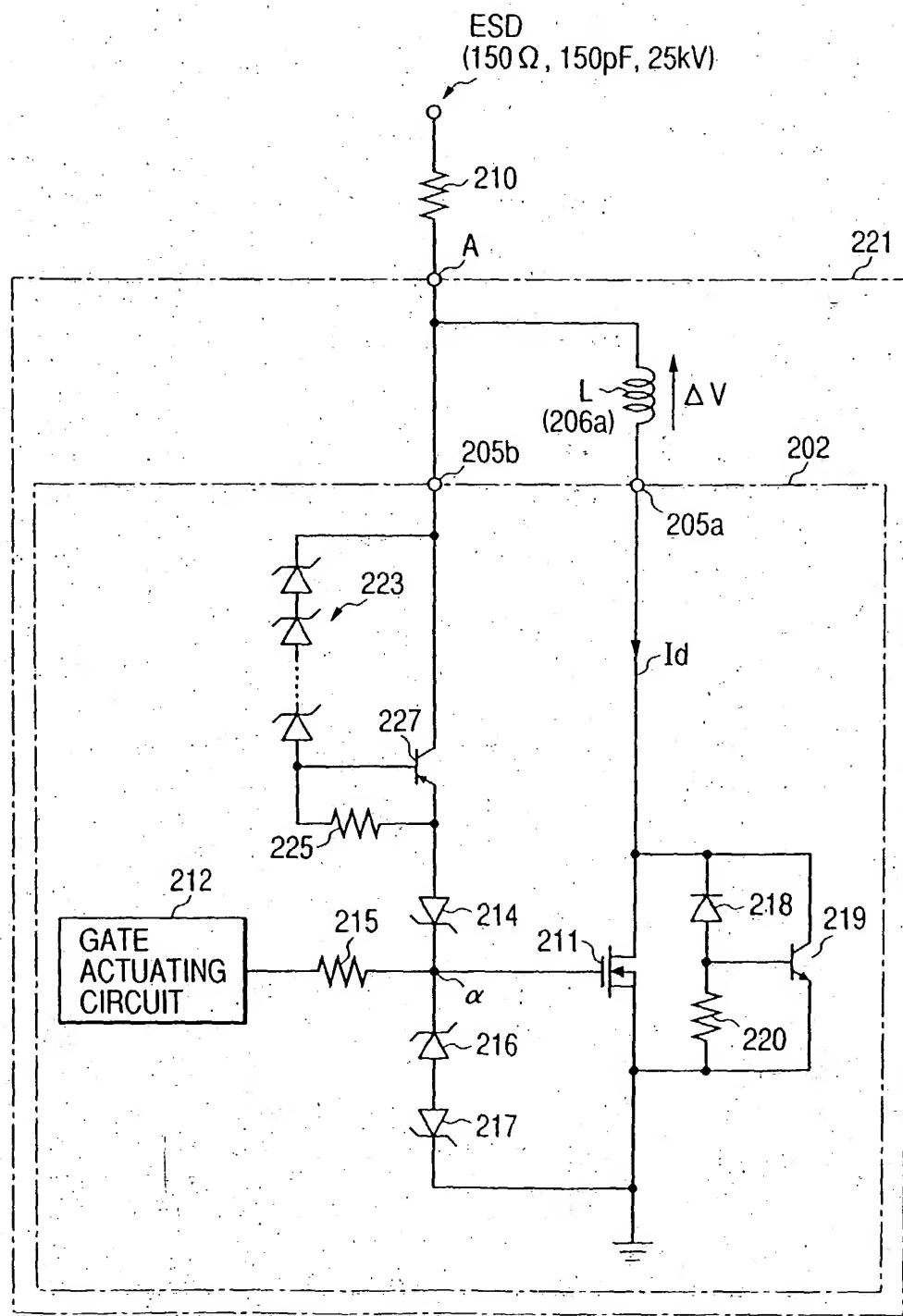
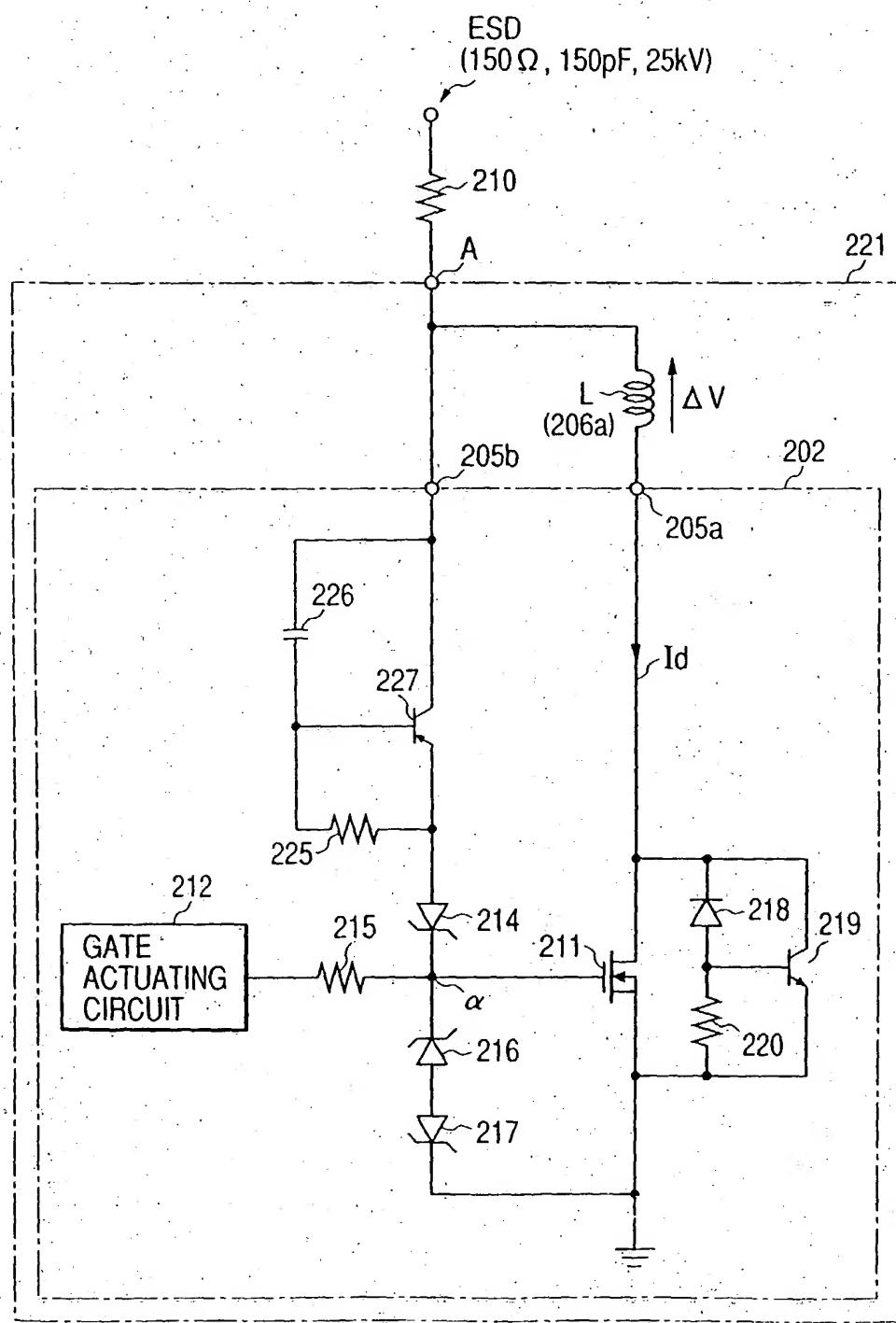


FIG. 28



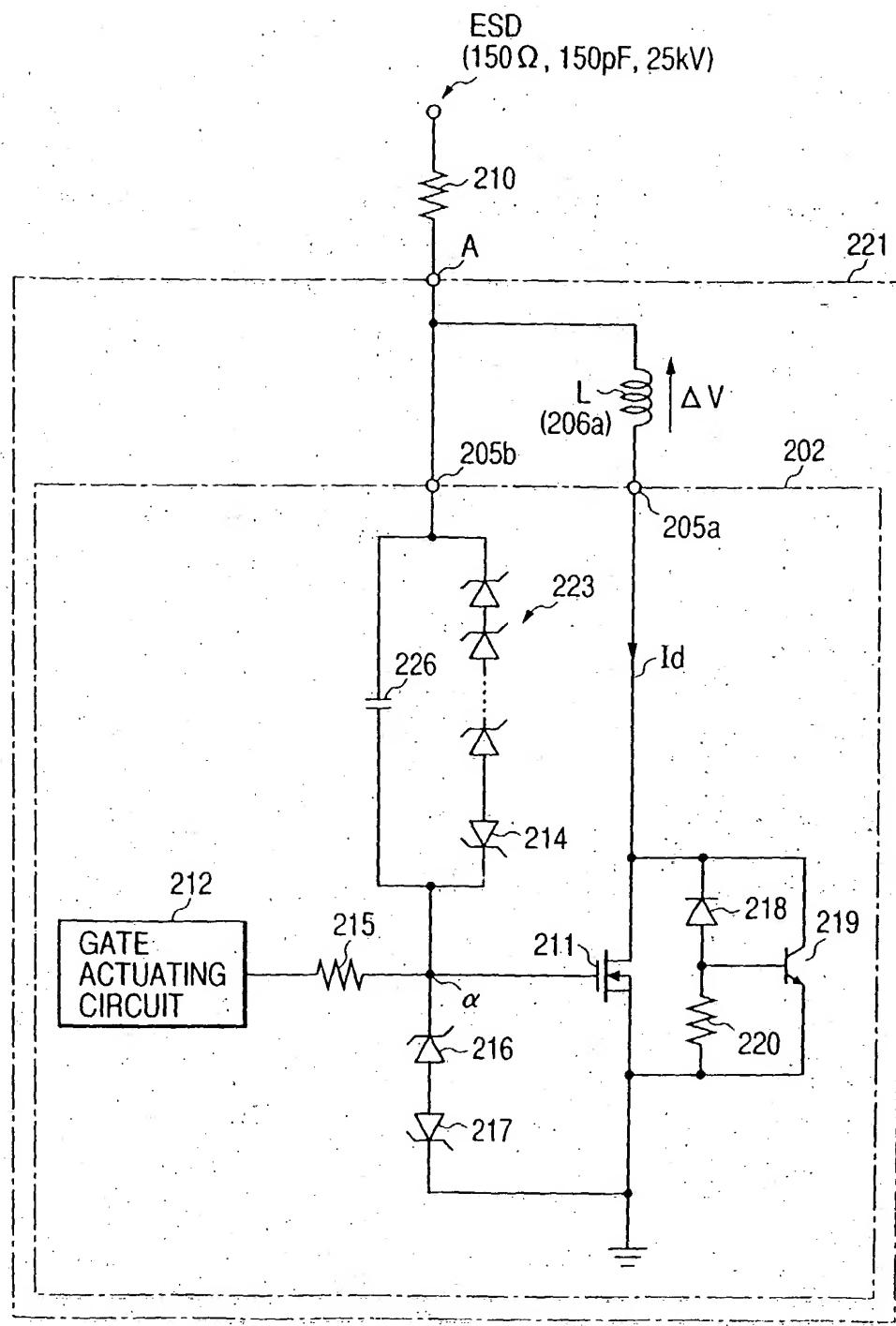
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FIG. 29



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FIG. 30



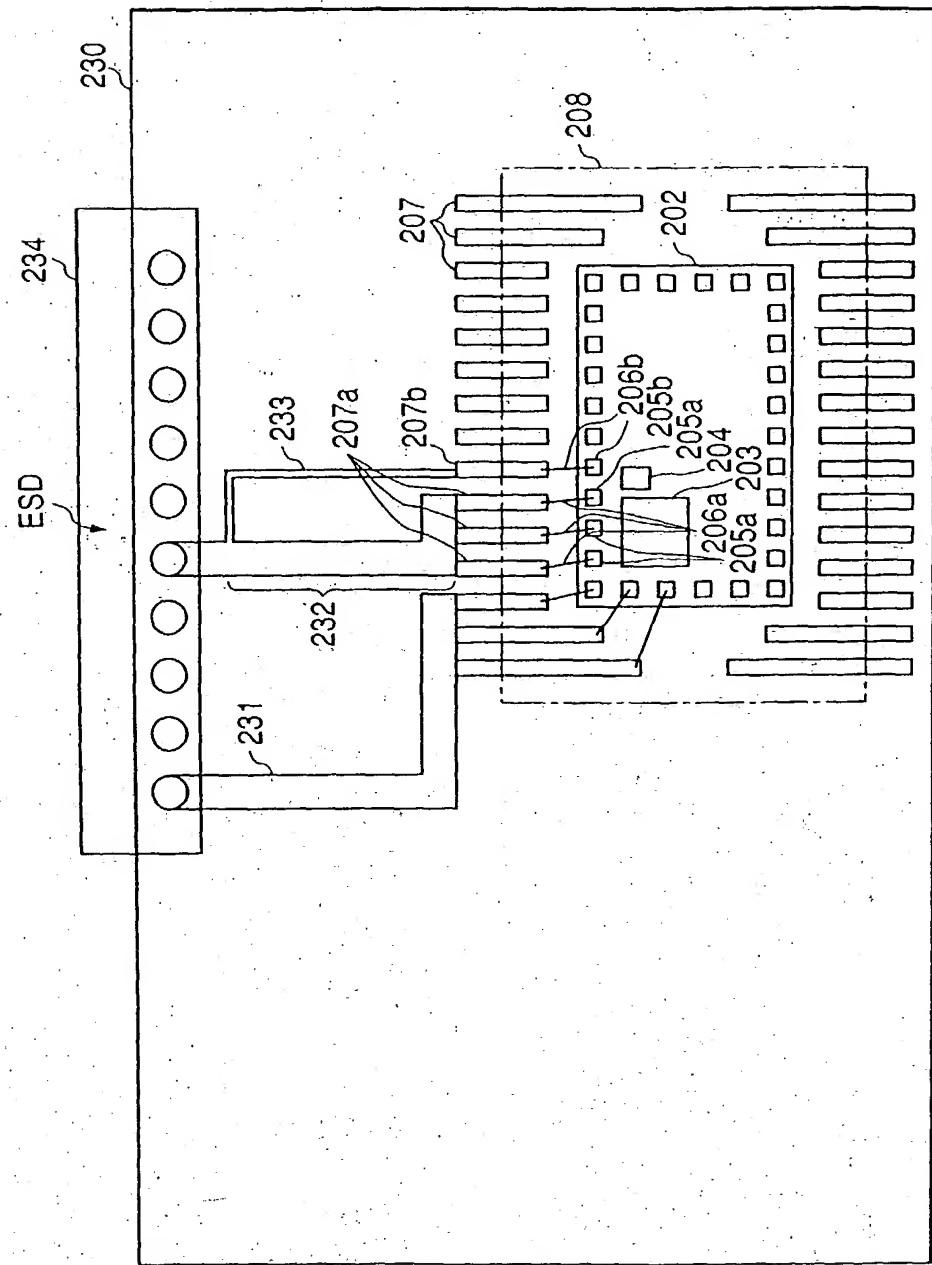
Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

Atty. Ref. No.: 4041P-000020/DVA

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FIG. 31



Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

Atty. Ref. No.: 4041P-000020/DVA

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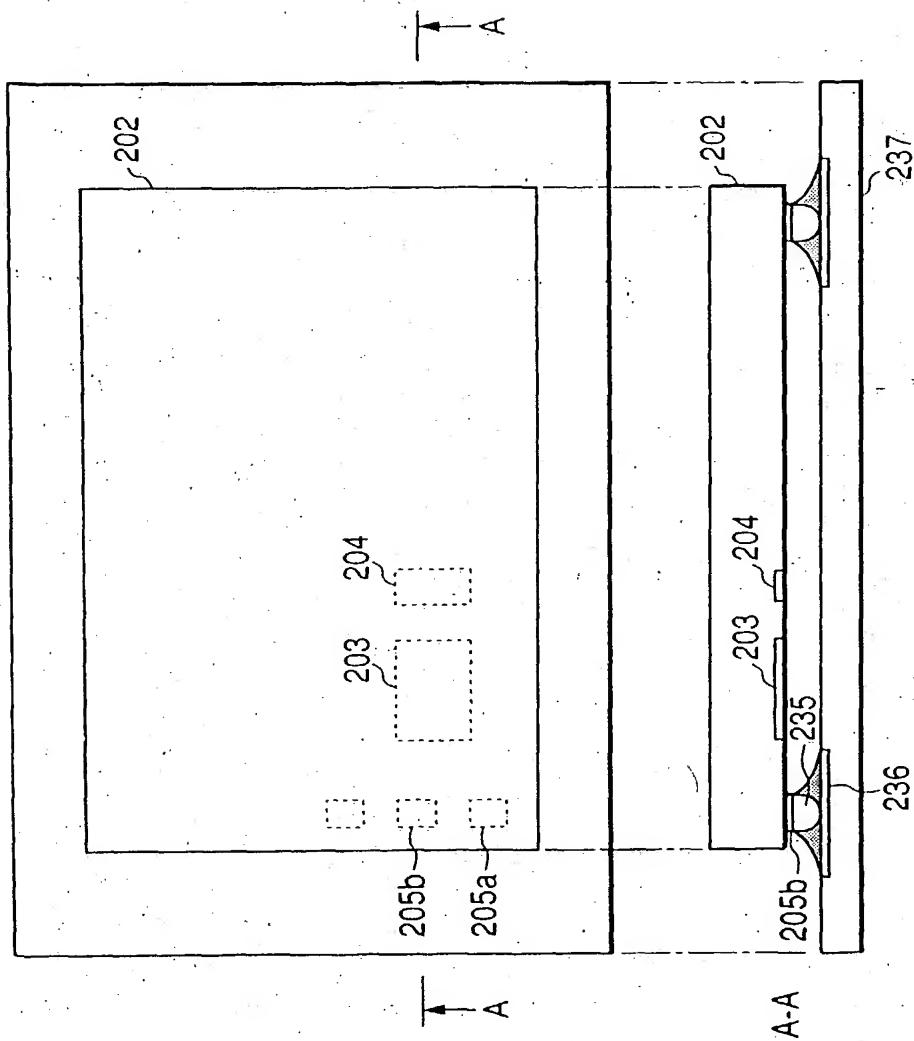


FIG. 32A

FIG. 32B

Title: Semiconductor Device Including A Surge Protecting Circuit

Inventor: Kenji Kohno

Atty. Ref. No.: 4041P-000020/DVA

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FIG. 33

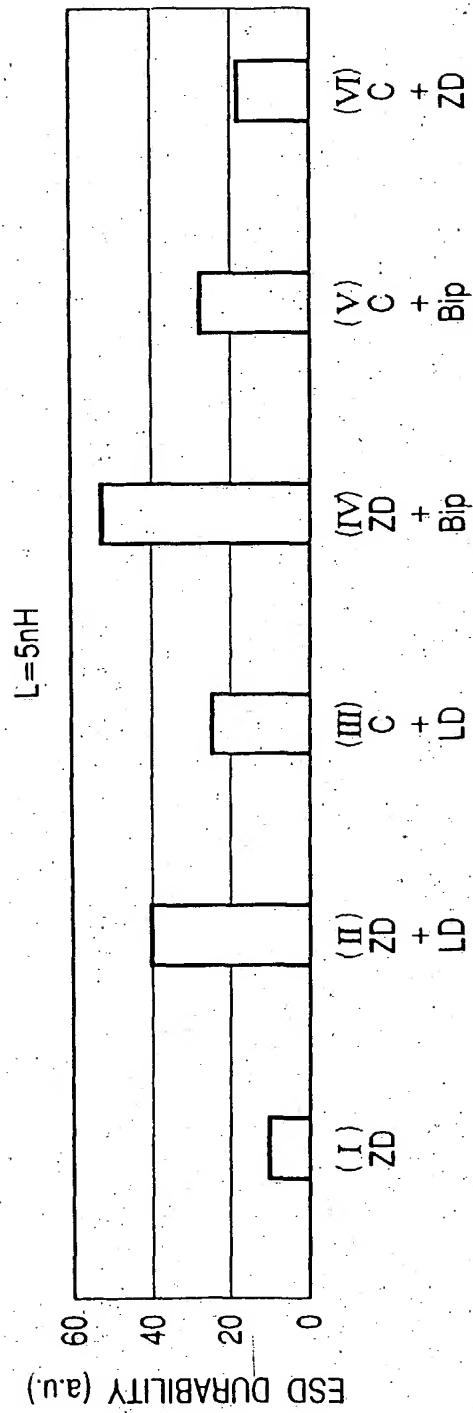


FIG. 34

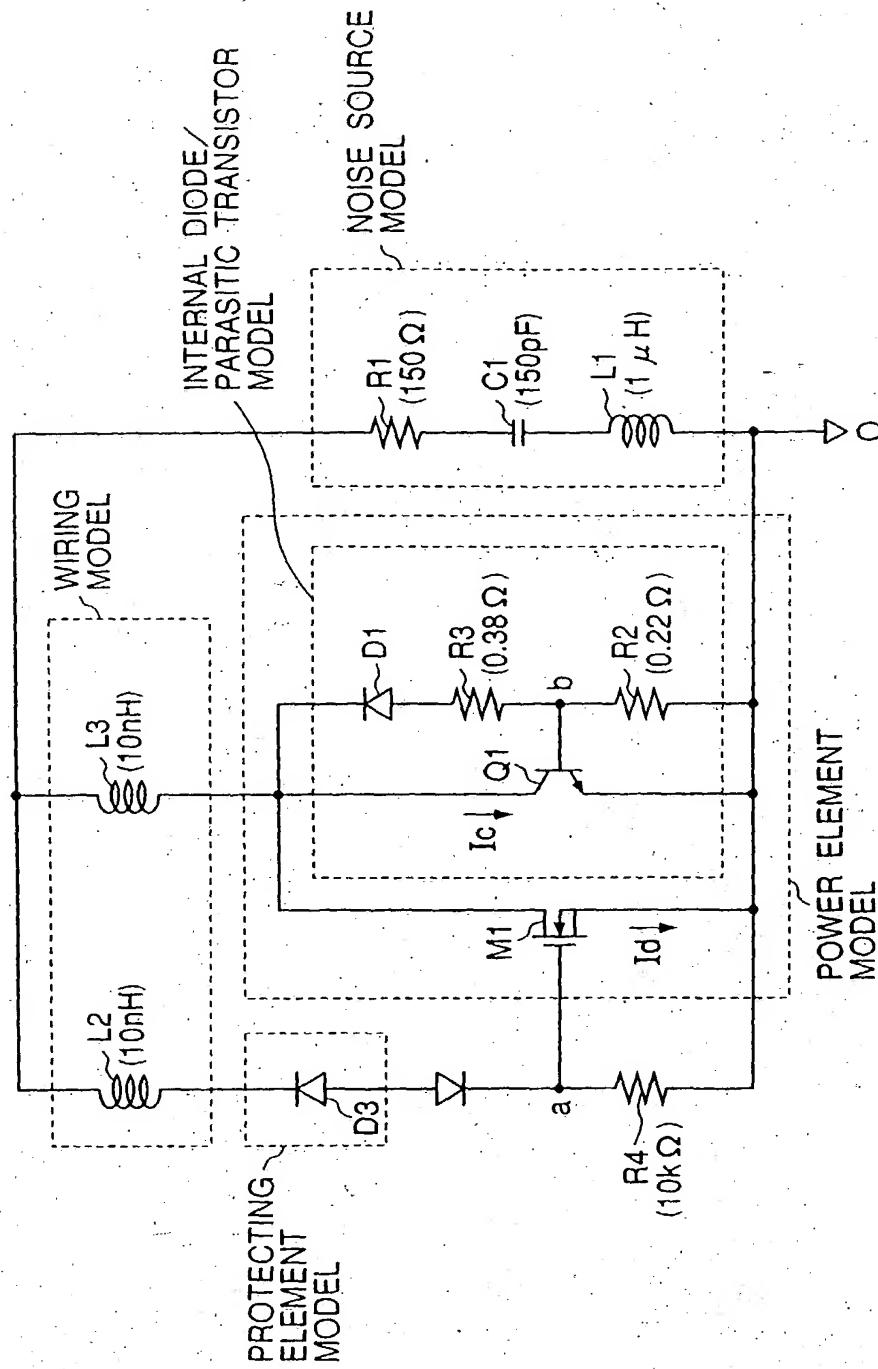
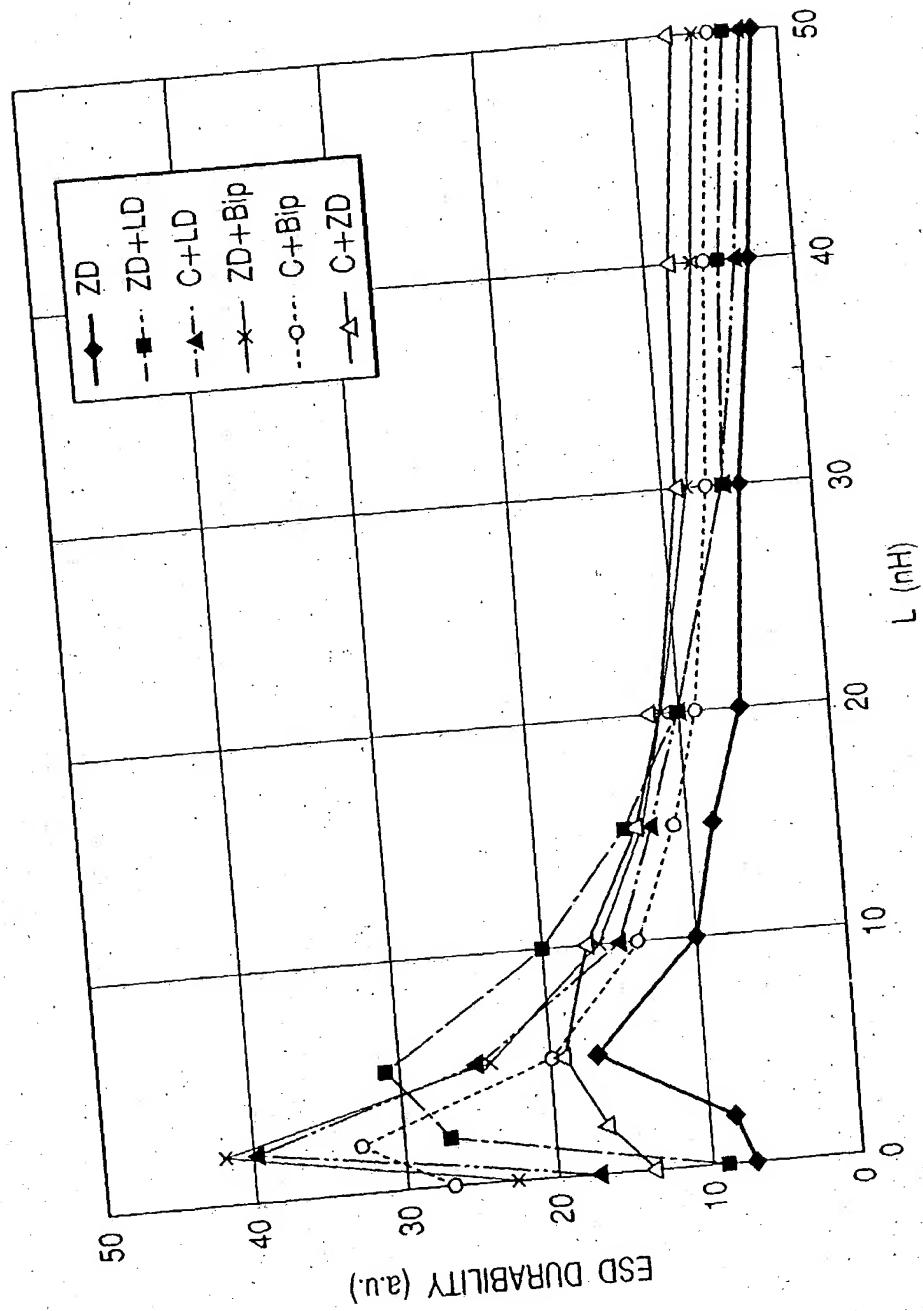


FIG. 35



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FIG. 36A

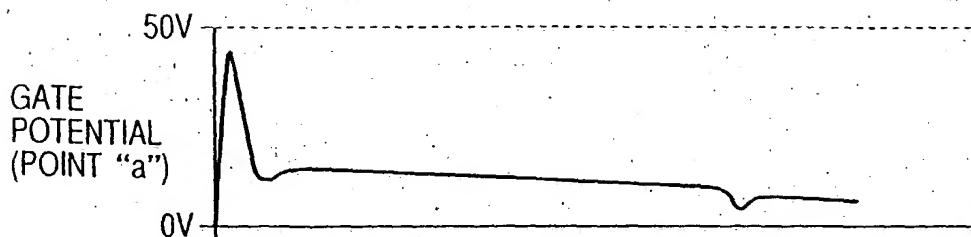


FIG. 36B

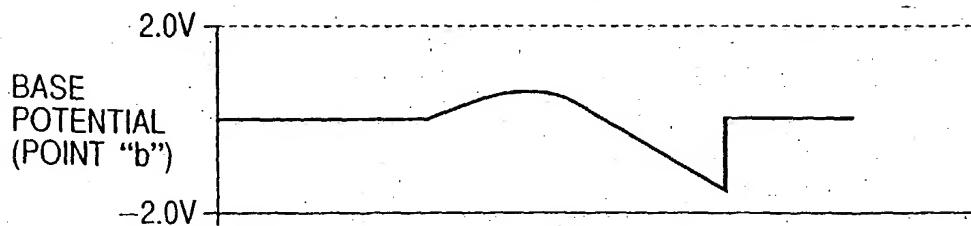


FIG. 36C

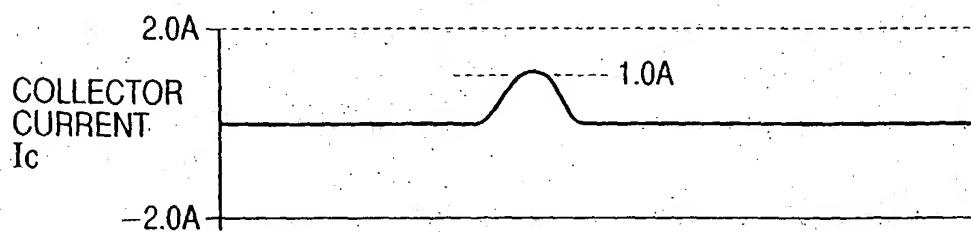


FIG. 36D

